PCT



WORLD INTELLECTUAL PROPERTY ORGANIZATIO: International Bureau



INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

Published

(51) International Patent Classification ⁶: G09G 3/20

A1

(11) International Publication Number:

WO 98/10405

| "

(43) International Publication Date:

12 March 1998 (12.03.98)

(21) International Application Number:

PCT/US97/15100

(22) International Filing Date:

27 August 1997 (27.08.97)

(30) Priority Data:

08/707,156

3 September 1996 (03.09.96) US

With international search report.

Before the expiration of the time limit for amending the claims and to be republished in the event of the receipt of amendments.

DK, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE).

(81) Designated States: CA, JP, European patent (AT, BE, CH, DE,

(71) Applicant: UNITED TECHNOLOGIES AUTOMOTIVE, INC. [US/US]; 5200 Auto Club Drive, Dearborn, MI 48126 (US).

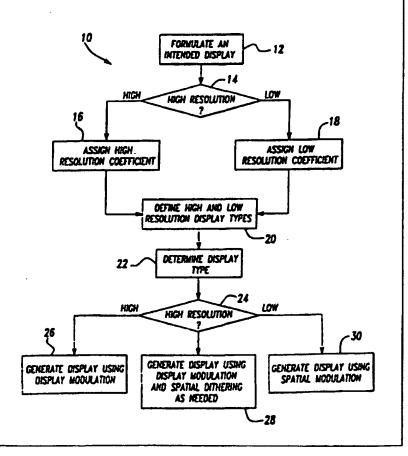
(72) Inventors: PALALAU, Silviu; 1445 Holland, Birmingham, MI 48009 (US). ROGERS, William; 10 Jacqueline Circle, Suffield, CT 06078 (US). TANSKI, William; 140 Hurlburt Street, Glastonbury, CT 06033 (US).

(74) Agents: OLDS, Theodore, W. et al.; Howard & Howard Attorneys, P.C., Suite 101, 1400 North Woodward Avenue, Bloomfield Hills, MI 48304 (US).

(54) Title: METHOD OF CONTROLLING DISPLAY IMAGE SHADING DEPENDING ON IMAGE RESOLUTION

(57) Abstract

A method of controlling display image shading includes determining whether an image requires high resolution or low resolution shading techniques. When high resolution is required, a frame modulation technique is used. When low resolution is required, a spatial dithering technique is used. This invention also includes partitioning a display into various portions where each portion may require high or low resolution. In such instances, a high resolution technique is used for portions of the display and a low resolution technique is used for other portions of the display as needed.



FOR THE PURPOSES OF INFORMATION ONLY

Codes used to identify States party to the PCT on the front pages of pamphlets publishing international applications under the PCT.

AL	Albania	ES	Spain	LS	Lesotho	SI	Slovenia
AM	Armenia	FI	Finland	LT	Lithuania	SK	Slovakia
AT	Austria	FR	France	LU	Luxembourg	SN	Senegal
AU	Australia	GA	Gabon	LV	Latvia	SZ	Swaziland
AZ	Azerbaijan	GB	United Kingdom	MC	Monaco	TD	Chad
BA	Bosnia and Herzegovina	GE	Georgia	MD	Republic of Moldova	TG	Togo
BB	Barbados	GH	Ghana	MG	Madagascar	Tj	Tajikistan
BE	Belgium	GN	Guinea	MK	The former Yugoslav	TM	Turkmenistan
BF	Burkina Faso	GR	Greece		Republic of Macedonia	TR	Turkey
BG	Bulgaria	HU	Hungary	ML	Mali	TT	Trinidad and Tobago
BJ	Benin	IE	Ireland	MN	Mongolia	UA	Ukraine
BR	Brazil	IL	Israel	MR	Mauritania	UG	Uganda
BY	Belarus	IS	Iceland	MW	Malawi	US	United States of America
CA	Canada	IT	Itały	MX	Mexico	UZ	Uzbekistan
CF	Central African Republic	JP	Japan	NE	Niger	VN	Viet Nam
CG	Congo	KE	Кепуа	NL	Netherlands	YU	Yugoslavia
CH	Switzerland	KG	Kyrgyzstan	NO	Norway	zw	Zimbabwe
CI	Côte d'Ivoire	KP	Democratic People's	NZ.	New Zealand		
CM	Cameroon		Republic of Korea	PL	Poland		
CN	China	KR	Republic of Korea	PT	Portugal		
CU	Cuba	KZ	Kazakstan	RO	Romania		
CZ	Czech Republic	LC	Saint Lucia	RU	Russian Federation		
DE	Germany	니	Liechtenstein	SD	Sudan		
DK	Denmark	LK	Sri Lanka	SE	Sweden		
EB	Estonia	LR	Liberia	SG	Singapore		

METHOD OF CONTROLLING DISPLAY IMAGE SHADING DEPENDING ON IMAGE RESOLUTION

BACKGROUND OF THE INVENTION

5

10

15

This invention relates to a method of controlling visual display image shading. More particularly, this invention relates to a method of choosing between alternative modes of image shading control depending on the level of resolution required for a particular visual display.

Various techniques are used for generating displays on electronic visual display panels. Some current techniques are more complex than others. The more complex techniques typically require more of a computer's memory and may result in higher energy usage within a given system. There is a need for improved utilization of various image shading control techniques, especially in circumstances where computer memory may be limited or power conservation is important.

This invention is a method of utilizing one or more image shading control techniques depending on the type of information to be displayed. More specifically, this invention is a method of choosing between image shading control techniques depending on whether a generated display requires high resolution or low resolution graphics.

SUMMARY OF THE INVENTION

25

30

20

In general terms, this invention is a method of controlling display image shading in an electrical visual display system having a display screen. The method includes several basic steps. First, two display types are defined; a first display type that requires high resolution and a second display type that requires low resolution. A determination is made whether an intended visual display is of the first display type or the second display type. When the intended display is a first display type, the display is generated on the display screen using a first image shading technique. When the intended display is a

second display type, the display is generated on the display screen using a second image shading technique. In the preferred embodiment, when a display requires high resolution, frame modulation is the preferred technique of image shading control. In the preferred embodiment, when the display requires low resolution, the preferred image shading technique is spatial dithering.

The various features and advantages of this invention will become apparent to those skilled in the art from the following detailed description of the presently preferred embodiment. The drawings that accompany the detailed description can be described as follows.

10

15

20

25

30

5

BRIEF DESCRIPTION OF THE DRAWINGS

Figure 1 is a schematic illustration of a visual display system.

Figure 2 is a flow chart diagram illustrating the methodology of this invention.

DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENT

Figure 1 schematically illustrates a visual display system including a computer memory and data processing module 4 and a video controller 6. Information from the computer memory and data processing module 4 is utilized by the video controller 6 to control a visual display displayed on a display screen 8. In the preferred embodiment, the display screen 8 is a flat panel display adapted to provide an electroluminescent display or a liquid crystal display, for example. The video controller 6 is also electrically coupled with various other input devices for receiving and processing information that may be used to modify or generate a display on the display screen 8. Given this specification, one skilled in the art can select from among commercially available microprocessors or develop specific circuitry to realize the data processing module 4 and the controller 6. Similarly, given this specification, one skilled in the art can develop specific code for

programming the controller 6 and module 4 for performing the functions associated with this invention.

Figure 2 contains a flow chart 10 that illustrates the methodology of this invention. This invention optimizes the use of gray shades and colors in flat panel displays by choosing between at least two types of techniques for controlling image shading depending on whether the image requires high resolution or low resolution. In the preferred embodiment, when high resolution is required, a frame modulation technique is utilized. Frame modulation provides high resolution but limited numbers of shades. When low resolution is sufficient, spatial dithering techniques preferably are used. Spatial dithering provides a larger number of shades or colors but cannot yield high resolution. The video controller 6 determines which technique to use based upon the resolution required for a particular display or portion of a display.

15

20

10

5

In Figure 2 an intended display is formulated at step 12. The intended display may include a variety of types of graphics. For example, if the intended display is a map, there would be graphical illustrations of roads or topography and overlying textual descriptions of roads and locations, for example. When formulating an intended display, the level of resolution that is required for a satisfactory display is determined at the step 14. When high resolution is required, such as for textual displays, a high resolution coefficient is assigned at 16. When low resolution is sufficient, a low resolution coefficient is assigned to a given display at 18. The steps 12 through 18 preferably are accomplished by the memory and data processing module 4 when formulating a databank or set of displays that will be generated on the display screen 8.

25

30

The video controller 6 defines high and low resolution display types at 20 depending on the contents of a particular display. In some instances, an entire display will be of one type or the other. In other words, some displays will require high resolution throughout the entire display, while others may require only low resolution throughout the entire display. This invention is not

5

10

15

20

25

-4-

limited to displays that require high or low resolution across an entire display. Some displays, such as the map example mentioned above, will be able to be generated using high resolution for portions of the display and low resolution for other portions. This invention includes utilizing separate image shading techniques for the various portions of such a display.

The video controller 6 determines the type of display at 22. A decision is made at 24 whether the display requires high resolution image shading. If it requires high resolution, then the display is generated using a frame modulation technique at 26. Developing specific circuitry or software for accomplishing a frame modulation technique can be developed by those skilled in the art and, therefore, need not be further described in this specification.

When low resolution should be used, the video controller 6 generates the display using a spatial dithering technique at 28. Developing specific circuitry or software for accomplishing a spatial dithering technique is within the scope of those skilled in the art and, therefore, need not be further described in this specification.

In those circumstances where a display requires high resolution for some portions of the display but low resolution for other portions, the video controller generates the display using frame modulation techniques and spatial dithering techniques as needed for the various portions of the display at 30.

The above description is exemplary rather than limiting in nature. Variations and modifications to the disclosed embodiment will become apparent to those skilled in the art that do not necessarily depart from the purview and spirit of this invention. Accordingly, the legal scope accorded to this invention can only be determined by studying the following claims.

CLAIMS

What is claimed is:

WO 98/10405

- 1. A method of controlling display image shading in an electrical visual display system having a display screen, comprising the steps of:
 - (A) defining a first display type as one requiring high resolution;
 - (B) defining a second display type as one requiring low resolution;
- (C) determining whether an intended visual display is a first display type or a second display type;
- (D) generating the intended display on the display screen using a first mode of image shading when the intended display is a first display type display; and
- (E) generating the intended display on the display screen using a second mode of image shading when the intended display is a second display type display.
- 2. The method of claim 1, wherein step (C) is performed by determining whether an entire intended display is a first or second display type display.

20

5

10

15

3. The method of claim 1, further comprising partitioning the intended display into portions and wherein step (C) is performed by determining whether each portion is a first or second display type display, respectively.

25

- 4. The method of claim 3, wherein steps (D) and (E) are performed with respect to each display portion separately.
- 5. The method of claim 1, wherein step (A) is performed by preselecting a high resolution coefficient value that corresponds to a display requiring high resolution and wherein step (B) is performed by preselecting a

5

10

15

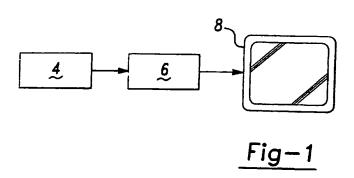
25

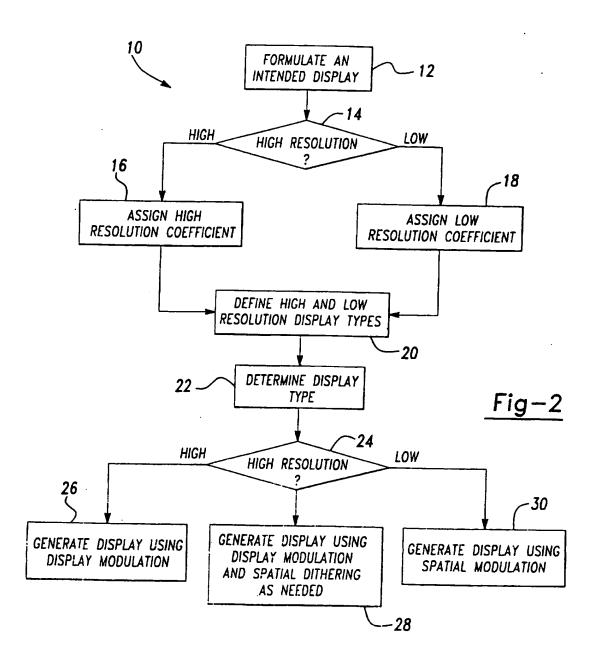
low resolution coefficient value that corresponds to a display requiring low resolution.

- 6. The method of claim 5, further comprising the step of preassigning a preselected resolution coefficient value to an intended display before performing steps (C) through (E).
- 7. The method of claim 6, wherein step (C) is performed by determining the preassigned resolution coefficient value of the intended display.
 - 8. The method of claim 5, further comprising the steps of partitioning the intended display into a plurality of intended display components, preassigning a preselected resolution coefficient value to each intended display component before performing steps (C) through (E) and wherein step (C) is performed by determining the preassigned resolution coefficient of each intended display component.
- 9. The method of claim 8, wherein step (D) is performed for the display components having a high resolution coefficient and wherein step (E) is performed for the display components having a low resolution coefficient.
 - 10. The method of claim 1, wherein step (D) is performed by using frame modulation.
 - 11. The method of claim 1, wherein step (E) is performed by using spatial dithering.
- 12. The method of claim 1, further comprising partitioning the intended display into a plurality of display portions wherein each display portion is either a first or second display type and wherein steps (D) and (E)

are simultaneously performed when at least one of the plurality of display portions is a first display type and at least one other display portion is a second display type and the one and one other display portions are simultaneously displayed on the display screen.

5





		FC1703	9//15100	
A. CLASS	FICATION OF SUBJECT MATTER G09G3/20			
		•		
According t	to International Patent Classification (IPC) or to both national class	ification and IPC		
	SEARCHED ocumentation searched (classification system followed by classification system followed by classif	estion europoic\	·····	
IPC 6	G09G	alon symbols)		
Documenta	tion searched other than minimum documentation to the extent th	at such documents are included in the field	s searched	
Electronic d	data base consulted during the international search (name of data	base and, where practical, search terms of	used)	
C. DOCUM	ENTS CONSIDERED TO BE RELEVANT		· · · · · · · · · · · · · · · · · · ·	
Category *	Citation of document, with indication, where appropriate, of the	relevant passages	Relevant to claim No.	
				
X	EP 0 673 012 A (CANON INFORMATI RESEARCH AUSTRALIA PTY LTD.) 20		1,2,11	
A	1995 see Abstract see page 3, line 11 - page 5, l	ine 3;	3,12	
i	figures 1-7 see page 8, line 36 - line 41 see page 9, line 11 - line 30 see page 9, line 48 - page 10,	line 11		
X	US 5 245 328 A (GARRETT) 14 Sep		1,2,10, 11	
	see Abstract see column 4, line 55 ~ column figures 1-4 see column 9, line 15 - line 36			
		-/		
X Furth	her documents are listed in the continuation of box C.	Patent family members are list	ted in annex.	
"A" docume	stegories of cited documents : ent defining the general state of the art which is not literate to be of particular relevance additional to the published on or after the international	T later document published after the or priority date and not in conflict cited to understand the principle of invention	with the application but or theory underlying the	
filing d "L" docume which		"X" document of particular relevance; cannot be considered novel or ca involve an inventive step when the "Y" document of particular relevance; it cannot be considered to involve a	nnot be considered to e document is taken alone the claimed invention	
"O" docume other r "P" docume	ent referring to an oral disclosure, use, exhibition or means ent published prior to the international filing date but	document is combined with one of ments, such combination being of in the art.	r more other such docu- bylous to a person skilled	
	actual completion of theinternational search	*&* document member of the same pa Date of malting of the international	· · · · · · · · · · · · · · · · · · ·	
19	9 December 1997	02/01/1998		
Name and n	nailing address of the ISA European Patent Office, P.B. 5818 Patentiaan 2 NL – 2280-HV Rijswijk	Authorized officer		
	Tel. (+31-70) 340-2040, Tx. 31 651 epo ni, Fax: (+31-70) 340-3016	Corsi, F		

1

Comban	Nion) DOCUMENTO CONCINENTE TO OF STATEMENT	PCT/US 97/15100	
ategory *	roundlen) DOCUMENTS CONSIDERED TO BE RELEVANT Citation of document, with indication where appropriate, of the relevant passages Relevant to claim No.		
	appropriate, or the release passages	MARASTE IO CISTILI NO.	
x	EP 0 421 772 A (CANON K.K.) 10 April 1991 see Abstract see column 2, line 25 - column 3, line 23; figure 10 see column 5, line 24 - column 6, line 12; figures 1,2	1,2	
	EP 0 570 906 A (CANON K.K.) 24 November 1993 see Abstract see column 13, line 21 - column 15, line 7; figures 2,12~15	3,4,11,	

Form PCT/ISA/210 (continuation of second sheet) (July 1992)

		LC1/02 A1/12100	
Patent document cited in search report	Publication date	Patent family member(s)	Publication date
EP 673012 A	20-09-95	AU 679320 B AU 1476395 A JP 8278768 A AU 676418 B AU 1476495 A AU 681234 B AU 1476595 A AU 679609 B AU 1477095 A CA 2144480 A CN 1111360 A EP 0671648 A EP 0673011 A JP 8050287 A JP 8069005 A JP 8201778 A AU 683131 B AU 1476795 A CA 2144479 A CN 1111361 A EP 0671650 A JP 8050288 A AU 683710 B AU 1476995 A EP 0671649 A JP 8036188 A	26-06-97 26-10-95 22-10-96 06-03-97 05-10-95 21-08-97 05-10-95 03-07-97 05-10-95 12-09-95 20-09-95 20-02-96 12-03-96 30-10-97 05-10-95 12-09-95 20-02-96 20-11-97 05-10-95 13-09-95 20-02-96 20-11-97 05-10-95 13-09-95 06-02-96
US 5245328 A	14-09-93	US 5068649 A CA 1326081 A DE 68923594 D DE 68923594 T EP 0364307 A JP 2176718 A	26-11-91 11-01-94 31-08-95 04-04-96 18-04-90 09-07-90
EP 421772 A	10-04-91	JP 3123386 A AT 132287 T AU 638754 B AU 6385890 A CA 2027043 A,C DE 69024448 D	27-05-91 15-01-96 08-07-93 11-04-91 07-04-91 08-02-96

Patent document cited in search report	Publication . date	Patent family member(s)	Publication date
EP 421772 A		DE 69024448 T	23-05-96
		ES 2081942 T KR 9402237 B	16-03-96 19-03-94
		KR 9402345 B	23-03-94
EP 570906 A	24-11-93	JP 5323906 A	07-12-93
		JP 5323280 A	07-12-93
		JP 5323907 A	07-12-93
		US 5613103 A	18-03-97

This Page is Inserted by IFW Indexing and Scanning Operations and is not part of the Official Record

BEST AVAILABLE IMAGES

Defective images within this document are accurate representations of the original documents submitted by the applicant.

Defects in the images include but are not limited to the items checked:

□ BLACK BORDERS
□ IMAGE CUT OFF AT TOP, BOTTOM OR SIDES
□ FADED TEXT OR DRAWING
□ BLURRED OR ILLEGIBLE TEXT OR DRAWING
□ SKEWED/SLANTED IMAGES
□ COLOR OR BLACK AND WHITE PHOTOGRAPHS
□ GRAY SCALE DOCUMENTS
□ LINES OR MARKS ON ORIGINAL DOCUMENT
□ REFERENCE(S) OR EXHIBIT(S) SUBMITTED ARE POOR QUALITY
□ OTHER: ___

IMAGES ARE BEST AVAILABLE COPY.

As rescanning these documents will not correct the image problems checked, please do not report these problems to the IFW Image Problem Mailbox.